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S #	Updt	Database	Query	Time	Comment
<u>S8740</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and blow\$ air	2002-12-06 14:07:58	
<u>S8739</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and blowing air	2002-12-06 14:07:36	
<u>S8738</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and air blower	2002-12-06 14:07:11	
<u>S8737</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and air blow	2002-12-06 14:06:58	
<u>S8736</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and air	2002-12-06 13:29:21	
<u>S8735</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((burn-in (device or apparatus))and wafer) and air hole	2002-12-06 13:29:08	
<u>S8734</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(burn-in (device or apparatus)) and electrode	2002-12-06 12:25:57	
<u>S8733</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(burn-in (device or apparatus)) and wafer	2002-12-06 12:24:58	
<u>S8732</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	burn-in (device or apparatus)	2002-12-06 12:23:50	
<u>S8731</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	burn-in and heated air	2002-12-06 11:13:28	
<u>S8730</u>	<u>U</u>	USPT,PGPB,JPAB,EPAB,DWPI,TDBD	burn-in and air	2002-12-06	

		blower	10:42:14
S8729	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (((wafer and vacuum)and ((324/\$)!..CCLS.))and burn-in)and hole) and electrode	2002-12-06 10:15:57
S8728	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (((wafer and vacuum)and ((324/\$)!..CCLS.))and burn-in) and hole	2002-12-06 10:15:25
S8727	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD ((wafer and vacuum)and ((324/\$)!..CCLS.)) and burn-in	2002-12-06 10:15:06
S8726	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (wafer and vacuum) and ((324/\$)!..CCLS.)	2002-12-06 10:14:49
S8725	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD wafer and vacuum	2002-12-06 10:14:34
S8724	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD ((wafer and holding plate)and through hole) and ((324/\$)!..CCLS.)	2002-12-06 10:06:52
S8723	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (wafer and holding plate) and through hole	2002-12-06 10:06:33
S8722	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD wafer and holding plate	2002-12-06 10:06:16
S8721	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD wafer and holding plat	2002-12-06 10:06:05
S8720	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (((program\$ logic device or PLD)and memory cell)and interconnect) and heat\$3	2002-12-05 12:58:18
S8719	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (((program\$ logic device or PLD)and memory cell)and interconnect) and heat\$	2002-12-05 12:55:12
S8718	U	USPT,PGPB,JPAB,EPAB,DWPI,TDBD (((program\$ logic device or PLD)and memory cell)and interconnect) and test signal) and burn-in	2002-12-05 12:24:10

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